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U.S. UTILITY Patent Application

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/653272	D	700	164	2121	184

Salman Akram Warren Farnworth Derek Gouchnour David Hembree

Method for using data regarding manufacturing procedures integrated circuits (IC'S) have undergone, such as repairs, to select procedures the IC'S will undergo, such as additional repairs 1289

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TERMINAL	DRAWINGS	CLAIMS ALLOWED
DISCLAIMER 305/653.272	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims Print Claim for O.G.
Lethe term of this patent (date)		NOTICE OF ALLOWANCE MAILED
nat been disclaimed.  The term of this patent shall not extend beyond the expiration date	(Assistant Examiner) (Date)	
6,0.8 Patent. No. 5, 907, 492 6,363, 295		ISSUBFEE
Contains	(Primary Examiner) (Date)	
The terminal months of this patent have been disclaimed.	(Legal instruments Examiner) (Osto)	¥ISSUE BATCHNUMBER
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